

**Final Projects - Presentations**

May 10, 2017

Time	Title / Authors(s)
7:20 – 7:35	<b>Timing and power optimization of an active clamping circuit in a low side driver power output</b> Antonio Rodríguez Castañeda

May 17, 2017

Time	Title / Authors(s)
7:10 – 7:25	<b>Extracción de parámetros para el modelado de una batería de ácido plomo a diferentes corrientes basado de un modelo de una batería de litio-ion</b> Omar Gallardo García
7:25 – 7:40	<b>Extracción de parámetros para un filtro rechaza banda con respuesta Chebyshev implementado con microcintas</b> Diego Zúñiga Valdez
7:40 – 7:55	<b>Analog fault modeling with artificial neural networks</b> Andrés Viveros Wachter
7:55 – 8:10	<b>Optimization of a bandpass filter using a commercial operational amplifier</b> Ernesto Morales Guerrero
8:10 – 8:25	<b>Characterization of planar interconnects based on the pad modeling by optimization method</b> Jacqueline Sánchez Mesa
8:25 – 8:40	<b>Anti-pad calculation for a high speed PCB using optimization methods</b> Adriana Galindo Vergara and Jaime de la Torre Aguirre
8:40 – 8:55	<b>Break</b>
8:55 – 9:10	<b>EPCIE2 aggressive space mapping equalizer optimization</b> Jesús Gómez López
9:10 – 9:25	<b>Optimización de un inductor espiral usando mapeo espacial (APLAC/PSI3D)</b> Marisol Cabrera Gómez
9:25 – 9:40	<b>Optimization of a single-stub shunt tuning network through space mapping</b> Edgar Rodríguez Jiménez and Manuel Cantor González
9:40 – 9:55	<b>Output capacitor optimization for an LDO linear regulator using space mapping</b> Baruch Cárdenas Ruvalcaba